

Amendments to the Claims:

The listing of claims will replace all prior versions, and listings, of claims in the application:

5

Listing of Claims:

Claim 1 (original): An in-plane switching mode liquid crystal display (IPS-LCD) comprising:

10 a lower substrate;

a plurality of parallel scan lines and a plurality of data lines with equal distances positioned on the lower substrate, wherein the scan lines and the data lines are arranged in a crossing manner to form a pixel matrix, any two of the adjoining scan lines and any two of the adjoining data lines being crossed to define a pixel;

15 a plurality of first electrodes formed in each of the pixels, wherein each of the first electrodes contains a plurality of first electrode offshoots, the first electrode offshoots being arranged parallel with each other and at two different planes on the lower substrate, wherein at least one of the first electrode offshoots on the higher plane overlaps another first electrode offshoot on the lower plane, and the two
20 overlapping first electrode offshoots have same shapes;

an insulation layer covering the scan lines and the first electrode offshoots at the lower plane;

25 a plurality of second electrodes formed in each of the pixels, wherein each of the second electrodes covers at least one of the first electrode offshoots in each of the pixels;

an upper substrate formed in parallel with and opposite to the lower substrate; and

a plurality of liquid crystal molecules filled between the upper substrate and the lower substrate;

30 wherein an overlapping portion of each of the first electrode offshoots and each of the second electrode serves as a storage capacitor of each of the pixels.

Claim 2 (original): The IPS-LCD of claim 1, wherein the first electrode offshoots are parallel with the data lines in each of the pixels.

5 Claim 3 (original): The IPS-LCD of claim 1, wherein the second electrodes partially cover the scan lines, an overlapping portion of each of the second electrodes and each of the scan lines serving as the storage capacitor of each of the pixels.

10 Claim 4 (original): The IPS-LCD of claim 1, wherein each of the second electrodes contains a plurality of second electrode offshoots, each of the second electrode offshoots being arranged parallel with the first electrode offshoots and covering one of the first electrode offshoots in each of the pixels.

15 Claim 5 (original): The IPS-LCD of claim 1 further comprising a first polarizer and a second polarizer positioned on an upper surface of the upper substrate and a bottom surface of the lower substrate respectively.

20 Claim 6 (original): The IPS-LCD of claim 1 further comprising a first alignment film and a second alignment film on a bottom surface of the upper substrate and an upper surface of the lower substrate respectively.

Claim 7 (original): The IPS-LCD of claim 1, wherein each of the pixels further comprises a thin film transistor (TFT), the TFT serving as a switching device of the pixel.

25 Claim 8 (original): The IPS-LCD of claim 1, wherein each of the first electrodes is used as a common electrode in each of the pixels.

30 Claim 9 (original): The IPS-LCD of claim 1, wherein each of the second electrodes is used as a pixel electrode in each of the pixels.

Claim 10 (original): The IPS-LCD of claim 1, wherein each of the first electrodes and each of the second electrodes are disposed in a single-layer structure or a multi-layer

structure.

Claim 11 (original): The IPS-LCD of claim 1, wherein the first electrodes and the second electrodes comprise titanium (Ti), aluminum (Al), aluminum based alloy,
5 indium tin oxide (ITO), or other conductive materials.

Claim 12 (original): The IPS-LCD of claim 1, wherein the data lines, the first electrode offshoots, and the second electrodes are bended lines.

10 Claim 13 (currently amended): An IPS-LCD comprising:

a lower substrate;

a plurality of parallel scan lines and a plurality of data lines with equal distances positioned on the upper surface of the lower substrate, wherein the scan lines and the data lines are arranged in a crossing manner to form a pixel matrix, any two of the
15 adjoining scan lines and any two of the adjoining data lines being crossed to define a pixel;

a plurality of the first electrodes formed in each of the pixels, wherein each of the first electrodes contains a plurality of first electrode offshoots, the first electrode offshoots being arranged parallel with each other in the pixels and at two different
20 planes on the lower substrate, wherein at least one of the first electrode offshoots at ~~[[on]]~~ the higher plane covers another first electrode offshoot at ~~[[on]]~~ the lower plane, and the two overlapping first electrode offshoots have same shapes;

a plurality of capacitor electrodes positioned at the lower plane where some of the first electrode offshoots are positioned, ~~arranged parallel with the first electrode offshoots in the pixels~~, each of the pixels comprising at least one of the capacitor
25 electrodes;

an insulation layer covering the first electrode offshoots at the lower plane, the capacitor electrodes, and the scan lines ;

a plurality of second electrodes, each of the second electrodes covering at least
30 one of the capacitor electrodes formed in each of the pixels;

an upper substrate positioned on the lower substrate in parallel with and opposite to the lower substrate; and

a plurality of liquid crystal molecules filled between the lower substrate and the upper substrate;

wherein an overlapping portion of each of the second electrodes and each of the capacitor electrodes serves as a storage capacitor of each of the pixels.

5

Claim 14 (original): The IPS-LCD of claim 13, wherein the first electrode offshoots are parallel with the data lines in the pixels.

Claim 15 (original): The IPS-LCD of claim 13, wherein the second electrodes
10 partially cover the scan lines, an overlapping portion of each of the second electrodes and the scan lines serving as the storage capacitor of each of the pixels.

Claim 16 (original): The IPS-LCD of claim 13, wherein each of the second electrodes
15 comprises a plurality of second electrode offshoots, each of the second electrode offshoots being arranged parallel with the first electrode offshoots and covering one of the capacitor electrodes formed in each of the pixels.

Claim 17 (original): The IPS-LCD of claim 13 further comprising a first polarizer and
20 a second polarizer positioned on an upper surface of the upper substrate and a bottom surface of the lower substrate respectively.

Claim 18 (original): The IPS-LCD of claim 13 further comprising a first alignment
25 film and a second alignment film on a bottom surface of the upper substrate and an upper surface of the lower substrate respectively.

Claim 19 (original): The IPS-LCD of claim 13, wherein each of the pixels further
comprises a TFT, the TFT serving as a switching device of the pixel.

Claim 20 (original): The IPS-LCD of claim 13, wherein each of the first electrodes is
30 used as a common electrode of each of the pixels.

Claim 21 (original): The IPS-LCD of claim 13, wherein each of the second electrodes

is used as a pixel electrode of each of the pixels.

Claim 22 (original): The IPS-LCD of claim 13, wherein each of the first electrodes,
each of the second electrodes, and each of the capacitor electrodes are disposed in a
5 single-layer structure or a multi-layer structure.

Claim 23 (original): The IPS-LCD of claim 13, wherein each of the first electrodes,
each of the second electrodes, and each of the capacitor electrodes comprise titanium,
aluminum, aluminum based alloy, ITO, or other conductive materials.

10

Claim 24 (original): The IPS-LCD of claim 13, wherein the data lines, the first
electrode offshoots, the second electrodes, and the capacitor electrodes are bended
lines.

15 Claim 25 (previously presented): The IPS-LCD of claim 1, wherein the insulation
layer vertically spaces apart the two overlapping first electrode offshoots.

Claim 26 (previously presented): The IPS-LCD of claim 1, wherein the first electrode
offshoot at the higher plane, the second electrodes, and the data lines are positioned at
20 the same plane.

Claim 27 (previously presented): The IPS-LCD of claim 1, wherein the first electrode
offshoot at the lower plane and the scan lines are positioned at the same plane.

25 Claim 28 (previously presented): The IPS-LCD of claim 13, wherein the insulation
layer vertically spaces apart the two overlapping first electrode offshoots.

Claim 29 (previously presented): The IPS-LCD of claim 13, wherein the first
electrode offshoot at the higher plane, the second electrodes, and the data lines are
30 positioned at the same plane.

Claim 30 (previously presented): The IPS-LCD of claim 13, wherein the first

electrode offshoot at the lower plane and the scan lines are positioned at the same plane.

Claim 31 (new): The IPS-LCD of claim 13, wherein each of the capacitor electrodes is
5 one of the first electrode offshoots at the lower plane.

Claim 32 (new): The IPS-LCD of claim 13, wherein each of the capacitor electrodes is electrically connected to one of the first electrodes.

10 Claim 33 (new): The IPS-LCD of claim 13, wherein one of the capacitor electrodes in the n^{th} pixel is electrically connected to the n^{th} scan line or the $(n+1)^{\text{th}}$ scan line.